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㉘ IC card system.

㉙ An IC card system allows a correct access by a right terminal to a specifically designated memory area of an IC card (1). Depending upon the terminal to which the IC card (1) is applied, the IC card performs identification and selection of the right area to be connected with the terminal. The IC card with a number of thus-featured areas enables a card holder to use it for many different terminals.

M 1	Personal code	
M 2	Present address	
M 3	Permanent domicile	
M	Bank account number	} M A
	Balance at the bank	
	Post office account number	} M B
	Balance at the post office	
	Personal code	} M C
	Amount of debt	
	Term of debt	} M D
	Personal code	
	Account receivable	} M F
	Clinical record number	
	Health insurance card number	} M F
	Immigration record	

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Background of the Invention

This invention relates to an IC card and more particularly to an IC card system comprising an IC card-receiving means to transmit data to and from the IC card.

An IC-buried card has a data processing function and is assured to allow a wide range of applications. Due to its convenience, the IC card is assumed to replace conventional magnetic stripe cards (MS cards). With the use of an IC memory, the IC card in accordance with this invention is improved in data storage capacity and reliability of data-reading in comparison with the MS card. Thus featured IC card will promise the use by a holder for many different terminals. The current problem is, therefore, how to assure storage of a number of different data in the card.

Brief Summary of the Invention

In the IC card system in accordance with the present invention, a data storage region is provided the IC card. The region consisted of a plurality of areas, is to assure data exchange between the IC card and a terminal to which the IC card is applied. To be more specific, when applied to a terminal, the card receives data from the terminal, thereby assuring access to its own area of the

data storage region. When the card is applied to a different terminal, other area within the region is available.

It is an object of the present invention to provide an IC card which has advantageously a large storage capacity thereby assuring a use of one card for various terminals.

Another objective of this invention is to assure highly reliable performance and security by providing the card with exclusive data storage areas for the use with respective terminals. The object of this invention is achieved by the card system according to the attached claim and figures.
Brief Summary of the Drawings

The objective of the present invention will be clear by the following with reference to the accompanying drawings, in which:

Fig. 1 is an external view of the IC card to be applied for an IC card system according to the present invention,

Fig. 2 is a block diagram of the IC used,

Fig. 3 shows the structure of a major part of the ROM 21 of the IC,

Fig. 4 shows types of terminals which permit the use of the IC card,

Fig. 5 is a flow chart illustrating the operation procedure when the IC card is applied, and

Fig. 6 and Fig. 7 are detailed flow charts of the n1 and n6 respectively.

Description of the Invention

Fig. 1 is an external view of the IC card prepared for the use with the IC card system in accordance with the present invention. In this figure, the IC 2 is buried inside an IC card made of plastic. On the upper surface of the IC 2, 8 connectors are formed exposedly. In addition, on the front surface of the card, formed is the embossment 4, and on the back, formed is the magnetic stripe 5 to be used as a spare storage medium.

Fig. 2 is a block diagram of the IC 2. The IC 2 is composed of CPU 20, EE PROM 21 to perform electrical writing and erasing (hereinafter abbreviated as ROM), and RAM 22 for working storage. The connector 3 is consisted of the C1 through C8 and electric power is supplied to the CPU 20 by means of the terminals C4 and C8 when the IC card is inserted into the card-receiving means. A reset pulse and a clock signal are also supplied to the CPU 20 through the connector C7 and connector C6. Further, serial data exchange between the CPU 20 and the IC card receiving means is performed through the connector C2.

In the Fig. 3, the are M1 stores a personal code of the IC card. The area M2 and the area M3 stores the holder's present address and his/her permanent domicile. The region M constitutes data storage areas. The areas MA store a bank account number and the balance of the account. This area is

applicable to a bank terminal. In addition, the areas MB, MC, MD, ME and MF are applicable to a post office terminal, loan company terminal, on-credit-sale company terminal, hospital receptionist terminal, and passport checking terminal respectively. The region M is divided into six data areas, each of them is exclusively accessible to respective terminals.

In Fig. 4, each terminal is provided with an IC card receiving means to receive the IC card to perform data exchange therebetween. It should be noted, however, that the card-receiving means is provided with a contact unit for the contact with the connector 3 in Fig. 1. Then the data exchange is performed under the state where the contact unit is in contact with the connector 3.

The following describes the system operation when the IC card is applied.

As is shown in Fig. 5, when the IC card is applied, identification of the card is performed at the step n1 (simply n1 hereafter). When the card is acceptable, a data transfer link is established for data exchange. The data stored in the IC card cannot be updated at this state. When the card is not acceptable, the card is rejected at the n9 and the operation is terminated upon the rejection of the card.

When the IC card is determined acceptable, a password is requested at the n3. When the transaction mode

is a payment mode, the amount of payment is requested to be input at the n4. Then, data transmission is made between the terminal and the terminal center to check the password or to update the date file of the terminal center. The updated data is returned to the IC card where the data is input in the memory (n5). At this time, the area MA responsive to the bank terminal A is available for access. In other words, each storage area within the MB through MF is kept inaccessible thereby assuring the exclusive access to the MA.

Upon completion of the processing at the n1, cash for the requested amount at the n4 is paid out (n7). A statement is issued (n8) and then the IC card is released (n9).

In Fig. 6, the left column flow chart shows the processing for the bank terminal A while the right column chart shows the processing for the IC card. With insertion of the IC card, electric power is supplied from the card receiving means through the connectors C4 and C8 at the n20. In addition, a reset pulse is supplied through the connector C7. The IC card operation program, pre-stored in the ROM 21, starts checking the correct function of hardware involved at the n40. This is a step to check the hardware thereby determining whether or not the IC card is properly applied to the card receiving means. Otherwise, the IC card will be rejected. For example, the rejection happens if the IC card

is erroneously inserted into an MS card reader. When the card receiving means receives unacceptable result from the IC card, the n22 determines the card is unacceptable. Then a system link is established and the system elects the n23 if the check result is acceptable. Otherwise, the system elects the n30 to cause the card to be rejected and returns to the initial state. In this case, the system elects the n2 through n9 shown in Fig. 5 to release the card for the termination of the operation.

When the hardware checking shows an acceptable result at the n22, a response signal is sent (n23) and terminal data is sent to the IC card (n24). When the IC card receives the signal for acceptable results of the hardware checking and terminal data, the terminal data received is identified at the n43 and the data storage area applicable thereto is released (n44). As the terminal in use is the bank terminal A, the terminal data to be received by the IC card is the data representing the bank terminal A. The data storage region being accessible is the area MA, to which the access is allowed exclusively.

When the area MA is accessible, request for the bank code and the password are input to the terminal at the n45. After input of these data the terminal transmits the corresponding data to the IC card (n26). The IC card checks the data received from the terminal (n47), then the IC card sends to the terminal a bank account number stored in the

are MA and the bank number (not illustrated in Fig. 3) stored in the same area. Naturally this is the procedures where the check result is satisfactory after the identification of these numbers (n28). When they are correct in view of the registered data, card validation is performed at the n29. The IC card is then ready for the next command from the terminal (n49).

Through these operation, the steps n45 through n48 are actuated only when the terminal data sent from the terminal represents a bank terminal. If the data is not in coincidence with the designated terminal, these steps are not actuated while other steps start actuating. The data storage area then ready for access is the area which is exclusively responsive to the given terminal data.

Referring to Fig. 7, the data to be written in the area MA is edited at the n50 when the procedure reaches the n6. The written data is then transmitted together with a "write" command in return. Being ready for the next command, the IC card, when it receives a "write" command from the bank terminal, identifies that the received command is the exact command (n61). Following the "write" command, the transmitted data is written into the region MA. Access to the region MB through MF is of course inhibited at this state.

For the reasons described above, the IC card identifies a terminal data at the n43 and allows the access to the

respective data storage area by the identified terminal.

Thus, a single card enables a holder to use for multiple terminals while preventing access to wrong data storage areas which are applicable to other terminals.

WHAT IS CLAIMED IS

1. In a card system composed of an IC card having a memory region consisted of a number of memory areas, each of them being accessible only to a compatible terminal, and a terminal having an IC card-receiving means to execute data exchange between said IC card and said IC card-receiving means, an IC card system comprising:

a means for receiving from said IC card identification data which identifies the terminal to which said IC card is applied, and

a means for allowing access by said terminal to a right memory area in which data regarding the identified terminal is stored.

FIG.5

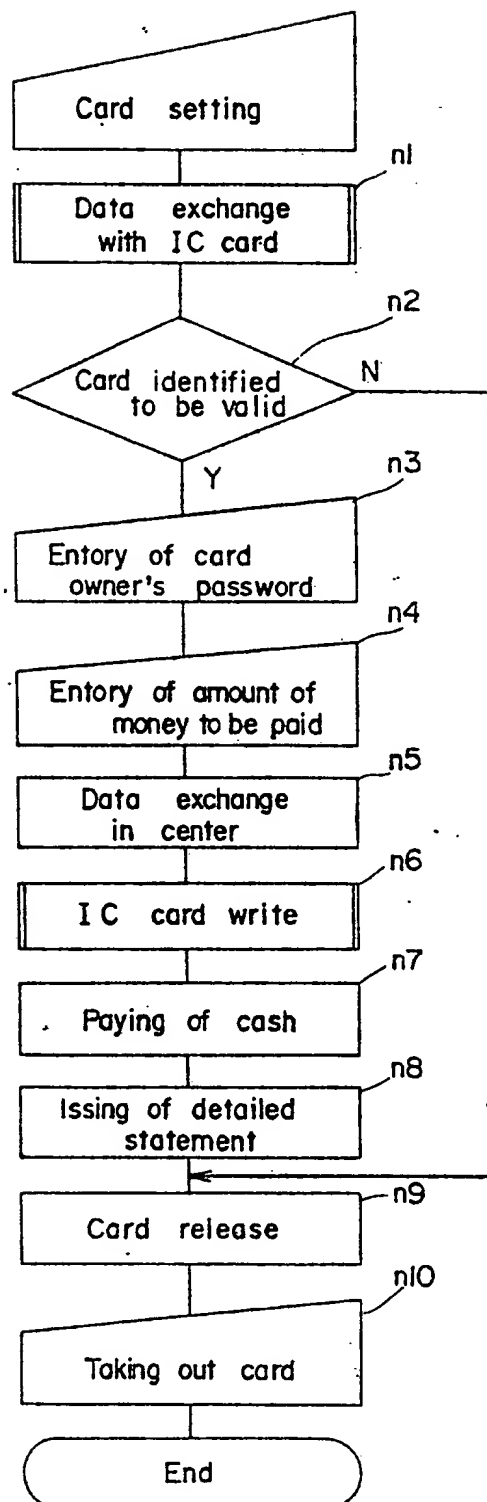


FIG. 1

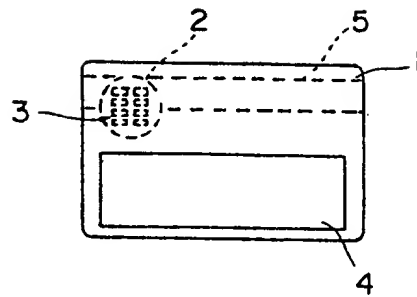
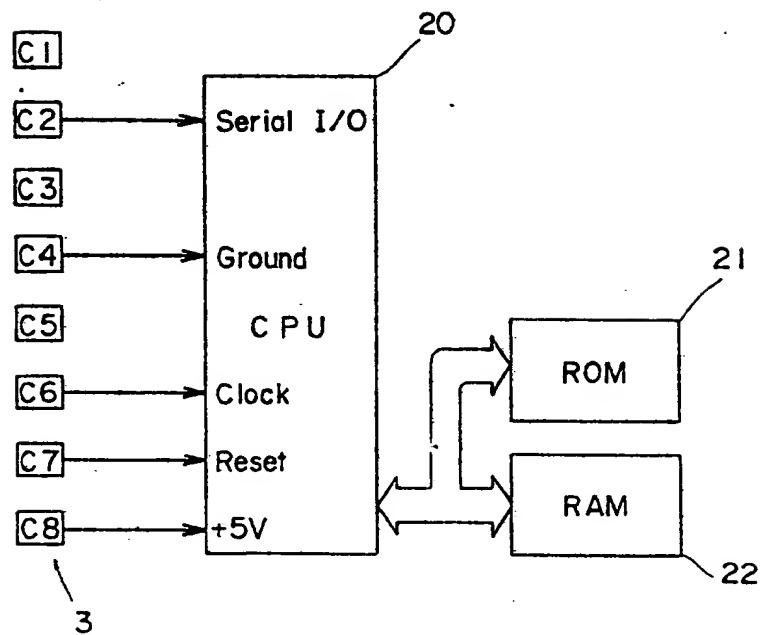


FIG. 2



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graph TD
    Start([Start]) --> HWCheck[Hardware check]
    HWCheck --> Wait1{Wait state}
    Wait1 --> Wait1
    Wait1 --> Wait2{Wait state}
    Wait2 --> Wait2
    Wait2 --> IDent[Identifying terminal type]
    IDent --> DecMA[Deciding to-be-released area of MA~MF areas]
    DecMA --> Request[Request for bankcode and password]
    Request --> Wait3{Wait state}
    Wait3 --> Wait3
    Wait3 --> Check[Checking bank code and password]
    Check --> Transmit[Transmitting bank account number and issuing bank number]
    Transmit --> Wait4{Wait state for next command}
    Wait4 --> Wait4
    Wait4 --> Processing[Processing for terminal of other type]
    Processing --> Wait4
    Wait4 --> IDent
    
    ICInput([IC card input means]) --> Power[Supplying electric power]
    Power --> Wait5{Wait state}
    Wait5 --> Wait5
    Wait5 --> HWOK{Hardware check result OK}
    HWOK -- N --> Wait1
    HWOK -- Y --> Coinc[Hardware coincidence response]
    Coinc --> TransmitType[Transmitting of terminal type]
    TransmitType --> Wait6{Wait state}
    Wait6 --> Wait6
    Wait6 --> TransmitBP[Transmitting of bank code and password]
    TransmitBP --> Wait7{Wait state}
    Wait7 --> Wait7
    Wait7 --> ValidCheck{Are bank account number and issuing bank number registered ones?}
    ValidCheck -- N --> Invalid[Card identified to be invalid]
    Invalid --> Wait4
    ValidCheck -- Y --> Valid[Card identified to be valid]
    Valid --> Return([Return])
  
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The flowchart illustrates the process of an IC card input means. It begins with 'IC card input means' leading to 'Supplying electric power' (n20). A 'Wait state' (n21) follows, leading to a decision 'Hardware check result OK' (n22). If 'N', it leads to the 'Wait state' (n41) in the 'Hardware check' section. If 'Y', it leads to 'Hardware coincidence response' (n23), then 'Transmitting of terminal type' (n24), and another 'Wait state' (n25). This leads to 'Transmitting of bank code and password' (n26), followed by a 'Wait state' (n27). A decision 'Are bank account number and issuing bank number registered ones?' (n28) follows. If 'N', it leads to 'Card identified to be invalid' (n30), which then leads to the 'Wait state for next command' (n49). If 'Y', it leads to 'Card identified to be valid' (n29), which leads to 'Return'. The 'Hardware check' section starts with 'Start' (n40), followed by 'Hardware check' (n41), 'Wait state' (n42), 'Identifying terminal type' (n43), 'Deciding to-be-released area of MA~MF areas' (n44), 'Request for bankcode and password' (n45), 'Wait state' (n46), 'Checking bank code and password' (n47), 'Transmitting bank account number and issuing bank number' (n48), and finally 'Wait state for next command' (n49). A 'Processing for terminal of other type' block is also shown, which leads to the 'Wait state for next command' (n49).

FIG.3

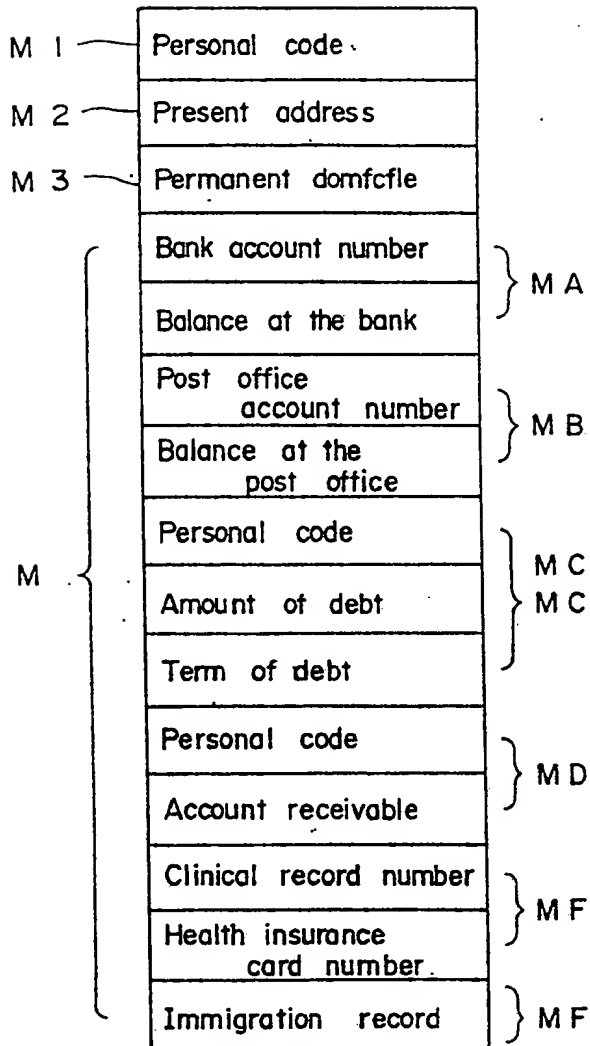


FIG.4

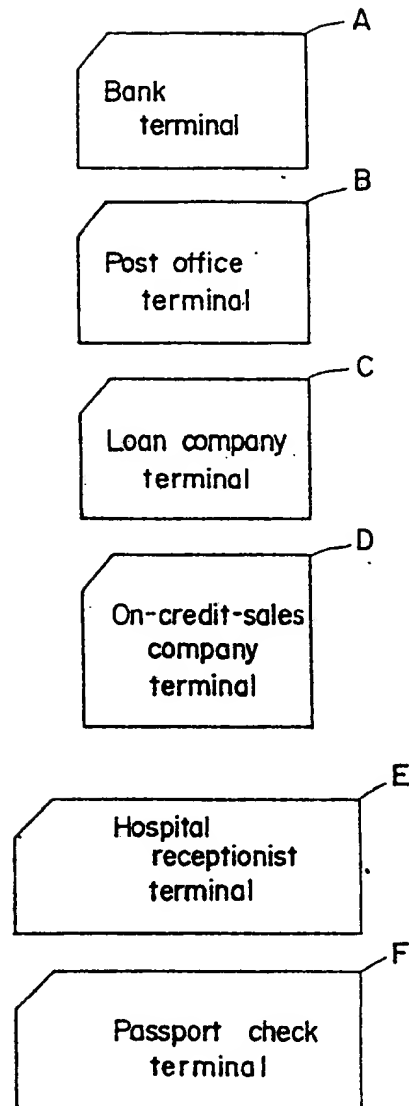
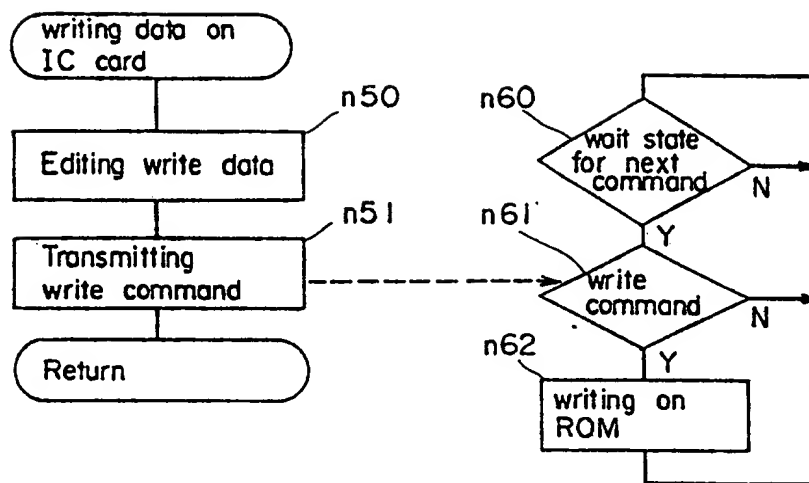


FIG. 7





European Patent
Office

EUROPEAN SEARCH REPORT

0193635

Application number

EP 85 10 2617

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int. Cl.4)
Y	EP-A-0 114 773 (CII HONEYWELL BULL) * Figures 1-3; page 4, line 35 - page 13, line 34 *	1	G 07 F 7/08
Y	FR-A-2 403 597 (CII HONEYWELL BULL) * Figur 1; page 3, line 7 - page 4, line 13 *	1	
A	EP-A-0 058 029 (HALPERN)	1	
A	US-A-4 001 550 (SCHATZ)	1	
The present search report has been drawn up for all claims			TECHNICAL FIELDS SEARCHED (Int. Cl.4)
			G 07 F
Place of search THE HAGUE		Date of completion of the search 07-11-1985	Examiner HERBELET J.C.
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